





FIG. 2b

Transform static high commissional transform
Transform static high supply voltage level into a static current
titto a statte carrent
Transform high-voltage input signal into
a proportional current signal
a proportional carrent signal
Combine said static current and current signal
into a resluting current input signal
Mirror said resulting current input signal
into a proportional current output signal 304
Generate low voltage output signal
representing said current output signal 305
Perform differential transactions upon said
voltage output signal, in relation to given 306
time steps
V
Compare low voltage differentiated signal with reference signal and generate a bit -307
with reference signal and generate a bit signal output
Process said bit signal within the digital
regulator part and generate output signal -308
Effect the conversion within the digital-
to-analog converter block -309
Feed output signal from digital-to-analog
converter as input to output driver -310
Close regulator loop with output signal
from output driver by feeding it back to 311
high voltage input signal
Drive load with high voltage output from
output driver
output ui toei

FIG. 3